S1C17 Family Technical Manual Errata

ITEM External connection for VPP							
Object manual	Document code	Object item	Page				
S1C17M01 Technical Manual	412361701	4.3.3 Flash Programming	4-3				
		17.2 Recommended Operating Conditions	17-1				
		18 Basic External Connection Diagram	18-1				
S1C17W03/04 Technical Manual	412925001	4.3.3 Flash Programming	4-3				
		21.2 Recommended Operating Conditions	21-1				
		22 Basic External Connection Diagram	22-1				
S1C17W14/16 Technical Manual	412910300	4.3.3 Flash Programming	4-3				
		22.2 Recommended Operating Conditions	22-1				
		23 Basic External Connection Diagram	23-1				
S1C17W15 Technical Manual	412645702	4.3.3 Flash Programming	4-3				
		20.2 Recommended Operating Conditions	20-1				
		21 Basic External Connection Diagram	21-1				
S1C17W22/23 Technical Manual	412690402	4.3.3 Flash Programming	4-3				
		23.2 Recommended Operating Conditions	23-1				
		24 Basic External Connection Diagram	24-1				
S1C17589 Technical Manual	412959200	4.3.3 Flash Programming	4-3				
		19.2 Recommended Operating Conditions	19-1				
		20 Basic External Connection Diagram	20-1				
S1C17656 Technical Manual	412745100	3.2.2 Flash Programming	3-2				

Flash Programming

(Error)

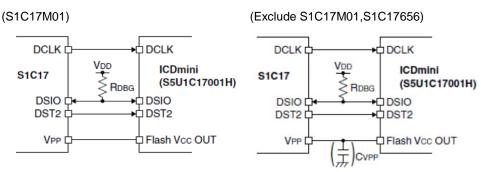


Figure 4.3.3.1 External Connection

Figure 4.3.3.1 External Connection

The VPP pin must be left open except when programming the Flash memory. However, it is not necessary to disconnect the wire when using ICDmini to supply the VPP power, as ICDmini controls the power supply so that it will be supplied during Flash programming only. CVPP should be connected if the VPP voltage is not

stable due to the effect of the distance between the VPP and Flash VCC OUT or other causes.

(Correct)

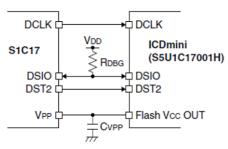


Figure 4.3.3.1 External Connection

The VPP pin must be left open except when programming the Flash memory. However, it is not necessary to disconnect the wire when using ICDmini to supply the VPP voltage, as ICDmini controls the power supply so that it will be supplied during Flash programming only. Be sure to connect CVPP for stabilizing the voltage when the VPP voltage is supplied externally.

Flash Programming (Only S1C17656)

(Error)

The S1C17656 supports on-board programming of the Flash memory, it makes it possible to program the Flash memory with the application programs/data by using the debugger through an ICDmini.

(Correct)

The S1C17656 supports on-board programming of the Flash memory, it makes it possible to program the Flash memory with the application programs/data by using the debugger through an ICDmini. Be sure to connect CVPP for stabilizing the voltage when the VPP voltage is supplied externally.

Recommended Operating Conditions

(Error)

(S1C17M01)

No description

(Exclude S1C17M01)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Capacitor between VSS and VPP	CVPP	* *	1	0.1	ı	μF

^{**} CVPP should be connected only when the VPP voltage is not stable.

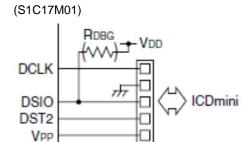
(X is 4-6)

(Correct)

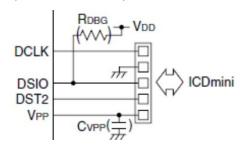
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Capacitor between VSS and VPP	CVPP		1	0.1	1	μF

Basic External Connection Diagram

(Error)



(Exclude S1C17M01)



(Correct)

