## S1C17 Manual errata

ITEM: Appendix D Measures Against Noise				
Object manuals	Document codes	Items	Pages	
S1C17M20/M21/M22/M23/ M24/M25 Technical Manual	413557000	Appendix D Measures Against Noise	AP-D-1	
S1C17M40 Technical Manual	413895200	Appendix D Measures Against Noise	AP-D-1	
S1C17F63 Technical Manual	413942900	Appendix D Measures Against Noise	AP-D-1	

(Error)

No description

#### (Correct)

Noise Measures for input terminals connected to signals with high drive capability such as power supplies

If there are terminals that are directly connected to the output of a power supply or a device with high drive capability, a large current may flow due to noise entering these terminals. In such a case insert a resistor of  $30\Omega$  or more in series to protect the terminals. Determine the resistance value after evaluation on the mounting board. When connecting the power supply directly to the VREFA terminal, insert a  $100\Omega$  resistor in series. In this case, there is no effect on the characteristics of the ADC.

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# S1C17 Manual errata

Object manuals	Document codes	Items	Pages
S1C17M01Technical Manual	412361701	8.4.2 Real-Time Clock Counter Operations	8-4
S1C17M10Technical Manual	413180200	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17M20/M21/M22/M23/M24/M25Techni cal Manual	413557000	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17M30/M31/M32/M33/M34Technical Manual	413495601	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17M40Technical Manual	413895200	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W03/W04Technical Manual	412925001	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W12/13Technical Manual	413520201	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W14/W16Technical Manual	412910300	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W15Technical Manual	412645702	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W18Technical Manual	413129601	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W22/W23Technical Manual	412690402	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W34/W35/W36Technical Manual	413237901	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17F63Technical Manual	413942900	21.4.2Real-Time Clock Function	21-6

(Error)

### Corrective operation when a value out of the effective range is set

When a value out of the effective range is set to the year, day of the week, or hour (in 24H mode) counter, the counter will be cleared to 0 at the next count-up timing. When a such value is set to the month, day, or hour (in 12H mode) counter, the counter will be set to 1 at the next count-up timing.

### (Correct)

#### Corrective operation when a value out of the effective range is set

When a value out of the effective range is set to the year, day of the week, or hour (in 24H mode) counter, the counter will be cleared to 0 at the next count-up timing <u>of the counter</u>. When a such value is set to the month, day, or hour (in 12H mode) counter, the counter will be set to 1 at the next count-up timing <u>of the counter</u>.

Note: <u>RTCMON.RTCMOH bits=0 & RTCMON.RTCMOL[3:0] bits=0x0 are prohibited.</u>