

S1C17 Manual errata

ITEM: Corrective operation when a value out of the effective range is set			
Object manuals	Document codes	Items	Pages
S1C17M01Technical Manual	412361701	8.4.2 Real-Time Clock Counter Operations	8-4
S1C17M10Technical Manual	413180200	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17M20/M21/M22/M23/M24/M25Technical Manual	413557000	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17M30/M31/M32/M33/M34Technical Manual	413495601	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17M40Technical Manual	413895200	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W03/W04Technical Manual	412925001	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W12/13Technical Manual	413520201	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W14/W16Technical Manual	412910300	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W15Technical Manual	412645702	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W18Technical Manual	413129601	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W22/W23Technical Manual	412690402	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17W34/W35/W36Technical Manual	413237901	9.4.2 Real-Time Clock Counter Operations	9-4
S1C17F63Technical Manual	413942900	21.4.2Real-Time Clock Function	21-6
(Error)			
<p>Corrective operation when a value out of the effective range is set</p> <p>When a value out of the effective range is set to the year, day of the week, or hour (in 24H mode) counter, the counter will be cleared to 0 at the next count-up timing. When a such value is set to the month, day, or hour (in 12H mode) counter, the counter will be set to 1 at the next count-up timing.</p>			

(Correct)

Corrective operation when a value out of the effective range is set

When a value out of the effective range is set to the year, day of the week, or hour (in 24H mode) counter, the counter will be cleared to 0 at the next count-up timing of the counter. When a such value is set to the month, day, or hour (in 12H mode) counter, the counter will be set to 1 at the next count-up timing of the counter.

Note: RTCMON.RTCMOH bits=0 & RTCMON.RTCMOL[3:0] bits=0x0 are prohibited.

S1C17 Manual errata

ITEM: Current consumption-frequency characteristics in RUN mode (OSC3 operation)

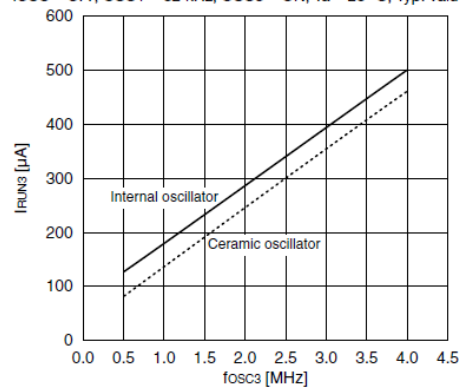
Object manuals	Document codes	Items	Pages
S1C17W18 Technical Manual	413129601	23.3 Current Consumption	23-3
S1C17W34/W35/W36 Technical Manual	413237901	23.3 Current Consumption	23-3
S1C17W12/W13 Technical Manual	413520201	21.3 Current Consumption	21-3

S1C17W12/W13/W18

(Error)

Current consumption-frequency characteristic in RUN mode (OSC3 operation)

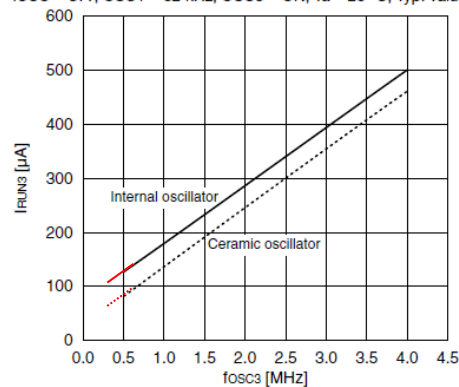
IOSC = OFF, OSC1 = 32 kHz, OSC3 = ON, Ta = 25 °C, Typ. value



(Correct)

Current consumption-frequency characteristic in RUN mode (OSC3 operation)

IOSC = OFF, OSC1 = 32 kHz, OSC3 = ON, Ta = 25 °C, Typ. value

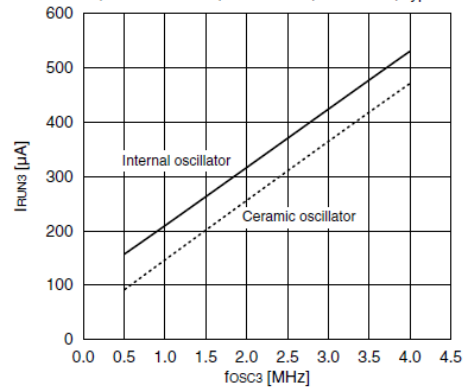


S1C17W34/W35/W36

(Error)

**Current consumption-frequency characteristic
in RUN mode (OSC3 operation)**

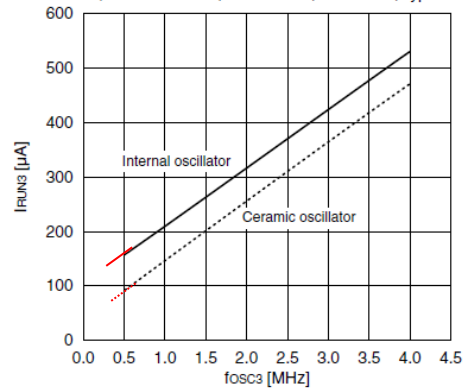
IIOSC = OFF, OSC1 = 32 kHz, OSC3 = ON, Ta = 25 °C, Typ. value



(Correct)

**Current consumption-frequency characteristic
in RUN mode (OSC3 operation)**

IIOSC = OFF, OSC1 = 32 kHz, OSC3 = ON, Ta = 25 °C, Typ. value



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ITEM: Real-Time Clock (RTCA) Theoretical Regulation Function			
Object manuals	Document codes	Items	Pages
S1C17M01Technical Manual	412361701	8.3.2 Theoretical Regulation Function	8-2
S1C17M10Technical Manual	413180200	9.3.2 Theoretical Regulation Function	9-2
S1C17M20/M21/M22/M23/M24 /M25 Technical Manual	413557000	9.3.2 Theoretical Regulation Function	9-2
S1C17M30/M31/M32/M33/M34 Technical Manual	413495600	9.3.2 Theoretical Regulation Function	9-2
S1C17W03/W04Technical Manual	412925001	9.3.2 Theoretical Regulation Function	9-2
S1C17W12/W13Technical Manual	413520201	9.3.2 Theoretical Regulation Function	9-2
S1C17W14/W16Technical Manual	412910200	9.3.2 Theoretical Regulation Function	9-2
S1C17W15Technical Manual	412645602	9.3.2 Theoretical Regulation Function	9-2
S1C17W18Technical Manual	413129501	9.3.2 Theoretical Regulation Function	9-2
S1C17W22/W23Technical Manual	412690302	9.3.2 Theoretical Regulation Function	9-2
S1C17W34/W35/W36Technical Manual	413237401	9.3.2 Theoretical Regulation Function	9-2
(Error)			
9.3.2 Theoretical Regulation Function			
<p>The time-of-day clock loses accuracy if the OSC1 frequency f_{OSC1} has a frequency tolerance from 32.768 kHz. To correct this error without changing any external part, RTCA provides a theoretical regulation function. Follow the procedure below to perform theoretical regulation.</p> <ol style="list-style-type: none"> 1. Measure the frequency tolerance “m [ppm]” of f_{OSC1}. 2. Determine the theoretical regulation execution cycle time “n seconds.” 3. Determine the value to be written to the RTCCTL.RTCTRM[6:0] bits from the results in Steps 1 and 2. 4. Write the value determined in Step 3 to the RTCCTL.RTCTRM[6:0] bits periodically in n-second cycles using an RTCA alarm or second interrupt. 5. Monitor the RTC1S signal to check that every n-second cycle has no error included. <p>The correction value for theoretical regulation can be specified within the range from -64 to +63 and it should be written to the RTCCTL.RTCTRM[6:0] bits as a two’s-complement number. Use Eq. 9.1 to calculate the correction value.</p>			

n: Theoretical regulation execution cycle time [second] (time interval to write the correct value to the RTCCTL.RTCTRM[6:0] bits periodically via software)
 m: OSC1 frequency tolerance [ppm]

(Correct)

9.3.2 Theoretical Regulation Function

The time-of-day clock loses accuracy if the OSC1 frequency f_{OSC1} has a frequency tolerance from 32.768 kHz. To correct this error without changing any external part, RTCA provides a theoretical regulation function. Follow the procedure below to perform theoretical regulation.

1. Determine the correction value of frequency tolerance “m [ppm] = - $\{(f_{OSC1}-32768[Hz]) / 32768[Hz]\} \times 10^6$ ” by measuring the f_{OSC1} .
2. Determine the theoretical regulation execution cycle time “n seconds.”
3. Determine the value to be written to the RTCCTL.RTCTRM[6:0] bits from the results in Steps 1 and 2.
4. Write the value determined in Step 3 to the RTCCTL.RTCTRM[6:0] bits periodically in n-second cycles using an RTCA alarm or second interrupt.
5. Monitor the RTC1S signal to check that every n-second cycle has no error included.

The correction value for theoretical regulation can be specified within the range from -64 to +63 and it should be written to the RTCCTL.RTCTRM[6:0] bits as a two's-complement number. Use Eq. 9.1 to calculate the correction value.

n: Theoretical regulation execution cycle time [second] (time interval to write the correct value to the RTCCTL.RTCTRM[6:0] bits periodically via software)
 m: OSC1's correction value of frequency tolerance [ppm]

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ITEM: RFC reference/sensor oscillation current consumption-power supply voltage characteristic			
Object manuals	Document codes	Items	Pages
S1C17W12/W13 Technical Manual	413520201	21.13 R/F Converter (RFC) Characteristics	21-16
<p>(Error)</p> <p>RFC reference/sensor oscillation current consumption-frequency characteristic (S1C17W13) <small>C_{REF} = 1,000 pF, T_a = 25 °C, Typ. value</small></p>			
<p>(Correct)</p> <p>RFC reference/sensor oscillation current consumption-frequency characteristic (S1C17W13) <small>C_{REF}=1000pF, T_a=25°C, Typ.</small></p>			

S1C17M10/M20/M21/M22/M23/M24/M25/M30/M31/M32/M33/M34/S7C17M11

(Error)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Reset hold time*1	tRSTR		-	-	200	uS

(Correct)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Reset hold time*1	tRSTR		<u>0.5</u>	-	<u>0.9</u>	mS

S1C17W03/W04/W12/W13/W15/W14/W16/W18/W22/W23/W34/W35/W36

(Error)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Reset hold time*1	tRSTR		-	-	1.7	mS

(Correct)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Reset hold time*1	tRSTR		<u>0.5</u>	-	<u>0.9</u>	mS

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ITEM: Absolute Maximum Ratings of #RESET pin																	
Object manuals	Document codes	Items	Pages														
S1C17M01Technical Manual	412361701	17.1 Absolute Maximum Ratings	17-1														
S1C17M10Technical Manual	413180200	19.1 Absolute Maximum Ratings	19-1														
S1C17M20/M21/M22/M23/M24 /M25 Technical Manual	413557000	21.1 Absolute Maximum Ratings	21-1														
S1C17M30/M31/M32/M33/M34 Technical Manual	413495600	23.1 Absolute Maximum Ratings	23-1														
S1C17W03/W04Technical Manual	412925001	21.1 Absolute Maximum Ratings	21-1														
S1C17W12/W13Technical Manual	413520201	21.1 Absolute Maximum Ratings	21-1														
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S1C17W22/W23Technical Manual	412690302	23.1Absolute Maximum Ratings	23-1														
S1C17W34/W35/W36Technical Manual	413237401	23.1Absolute Maximum Ratings	23-1														
S7C17M11Technical Manual	413393900	21.1Absolute Maximum Ratings	21-1														
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Input voltage	Vi	Pxx	-0.3~7.0	V													
		Pyy, #RESET	-0.3~V _{DD} +0.5	V													

(Error)

3.3.3 List of Debugger Input/Output Pins

The debugger input/output pins are shared with general-purpose I/O ports and are initially set as the debug pins. If the debugging function is not used, these pins can be switched to general-purpose I/O port pins. For details, refer to the “I/O Ports” chapter.

Note: Do not drive the DCLK pin with a high level from outside (e.g. pulling up with a resistor). Also, do not connect (short-circuit) between the DCLK pin and another GPIO port. In the both cases, the IC may not start up normally due to unstable pin input/output status at power on.

(Correct)

3.3.3 List of Debugger Input/Output Pins

The debugger input/output pins are shared with general-purpose I/O ports and are initially set as the debug pins. If the debugging function is not used, these pins can be switched to general-purpose I/O port pins. For details, refer to the “I/O Ports” chapter.

Note:

- Do not drive the DCLK pin with a high level from outside (e.g. pulling up with a resistor). Also, do not connect (short-circuit) between the DCLK pin and another GPIO port. In the both cases, the IC may not start up normally due to unstable pin input/output status at power on.
- Do not drive the DSIO pin with a low level from outside. Then the CPU enters DEBUG mode by a debug interrupt.

(Error)

RTC Month/Day Register

Bit 12 RTCMOH

Bits 11–8 RTCMOL[3:0]

The RTCMON.RTCMOH bit and the RTCMON.RTCMOL[3:0] bits are used to set and read the 10-month digit and the 1-month digit of the month counter, respectively. The setting/read values are a BCD code within the range from 1 to 12.

Note: Be sure to avoid writing to the RTCMON.RTCMOH/RTCMOL[3:0] bits while the RTCCTL.RTCBSY bit = 1.

(Correct)

RTC Month/Day Register

Bit 12 RTCMOH

Bits 11–8 RTCMOL[3:0]

The RTCMON.RTCMOH bit and the RTCMON.RTCMOL[3:0] bits are used to set and read the 10-month digit and the 1-month digit of the month counter, respectively. The setting/read values are a BCD code within the range from 1 to 12.

Note:

- Be sure to avoid writing to the RTCMON.RTCMOH/RTCMOL[3:0] bits while the RTCCTL.RTCBSY bit = 1.
- Be sure to avoid setting 0x00 to the RTCMON.RTCMOH/RTCMOL[3:0] bits.

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ITEM: Recommended Operating Conditions LCD power supply voltage																														
Object manuals	Document codes	Items	Pages																											
S1C17M10Technical Manual	413180200	19.2 Recommended Operating Conditions	19-1																											
S1C17W12/W13Technical Manual	413520201	21.2 Recommended Operating Conditions	21-1																											
S1C17W14/W16Technical Manual	412910200	21.2 Recommended Operating Conditions	22-1																											
S1C17W15Technical Manual	412645602	21.2 Recommended Operating Conditions	20-1																											
S1C17W18Technical Manual	413129501	21.2 Recommended Operating Conditions	23-1																											
S1C17W22/W23Technical Manual	412690302	21.2 Recommended Operating Conditions	23-1																											
S1C17W34/W35/W36Technical Manual	413237401	21.2 Recommended Operating Conditions	23-1																											
S1C17W12/W13/W14/W16																														
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	Vc3		-	3.0	5.4	V																								

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(Error)

項目	記号	条件	Min.	Typ.	Max.	単位
LCD power supply voltage (1/3bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{DD} \leq V_{c2} \leq V_{c3} (= V_{c4})$	-	1.0	1.8	V
	Vc2		-	2.0	3.6	V
	Vc3/Vc4		-	3.0	5.4	V
LCD power supply voltage (1/4bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{DD} \leq V_{c2} \leq V_{c3} \leq V_{c4}$	-	1.0	1.4	V
	Vc2		-	2.0	2.8	V
	Vc3		-	3.0	4.2	V
	Vc4		-	4.0	5.6	V

(Correct)

項目	記号	条件	Min.	Typ.	Max.	単位
LCD power supply voltage (1/3bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{c2} \leq V_{c3} (= V_{c4}), V_{c1} \leq V_{DD}$	-	1.0	1.8	V
	Vc2		-	2.0	3.6	V
	Vc3/Vc4		-	3.0	5.4	V
LCD power supply voltage (1/4bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{c2} \leq V_{c3} \leq V_{c4}, V_{c1} \leq V_{DD}$	-	1.0	1.4	V
	Vc2		-	2.0	2.8	V
	Vc3		-	3.0	4.2	V
	Vc4		-	4.0	5.6	V

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(Error)

項目	記号	条件	Min.	Typ.	Max.	単位
LCD power supply voltage (1/4bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{DD} \leq V_{c2} \leq V_{c3} \leq V_{c4} (= V_{c5})$	-	1.0	1.2	V
	Vc2		-	2.0	2.4	V
	Vc3		-	3.0	3.6	V
	Vc4/Vc5		-	4.0	4.8	V
LCD power supply voltage (1/5bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{DD} \leq V_{c2} \leq V_{c3} \leq V_{c4} \leq V_{c5}$	-	1.0	1.2	V
	Vc2		-	2.0	2.4	V
	Vc3		-	3.0	3.6	V
	Vc4		-	4.0	4.8	V
	Vc5		-	5.0	6.0	V

(Correct)

項目	記号	条件	Min.	Typ.	Max.	単位
LCD power supply voltage (1/4bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{c2} \leq V_{c3} \leq V_{c4} (= V_{c5}), V_{c2} \leq V_{DD}$	-	1.0	1.2	V
	Vc2		-	2.0	2.4	V
	Vc3		-	3.0	3.6	V
	Vc4/Vc5		-	4.0	4.8	V
LCD power supply voltage (1/5bias)	Vc1	When an external voltage is applied, $V_{c1} \leq V_{c2} \leq V_{c3} \leq V_{c4} \leq V_{c5}, V_{c2} \leq V_{DD}$	-	1.0	1.2	V
	Vc2		-	2.0	2.4	V
	Vc3		-	3.0	3.6	V
	Vc4		-	4.0	4.8	V
	Vc5		-	5.0	6.0	V

S1C17 Family Technical Manual Errata

ITEM 1.3 Pins			
Object manual	Document code	Object item	Page
S1C17W12/W13 Technical Manual	413520201	1.3.2 Pad Configuration Diagram (Chip)	1-10
<p>(Error)</p> <div style="display: flex; justify-content: space-between;"> <div style="width: 30%;"> <p>Port function or signal assignment</p> <p>P13/RFCLKO0/UPMUX/SEG19</p> <p>P14/EXCL10/UPMUX/SEG18</p> <p>P15/EXCL11/UPMUX/SEG17</p> <p>P16/UPMUX/SEG16</p> <p>P17/RTC1S/UPMUX/SEG15</p> <p>P40/SEG25</p> <p>P41/SEG24</p> <p>P42/SEG23</p> <p>P43/SEG22</p> <p>P20/EXCL00/UPMUX/SEG14</p> <p>P21/EXCL01/UPMUX/SEG13</p> <p>P22/UPMUX/SEG12</p> <p>P23/UPMUX/SEG11</p> <p>P24/UPMUX/SEG10</p> <p>P25/UPMUX/SEG9</p> <p>P26/UPMUX/SEG8</p> </div> <div style="width: 30%; text-align: center;"> <p style="text-align: center;">Y ↑ X → (0, 0)</p> </div> <div style="width: 30%;"> <p>Pad name</p> <p>P13 <input type="checkbox"/> 49</p> <p>P14 <input type="checkbox"/> 50</p> <p>P15 <input type="checkbox"/> 51</p> <p>P16 <input type="checkbox"/> 52</p> <p>P17 <input type="checkbox"/> 53</p> <p>P40 <input type="checkbox"/> 54</p> <p>P41 <input type="checkbox"/> 55</p> <p>P42 <input type="checkbox"/> 56</p> <p>P43 <input type="checkbox"/> 57</p> <p>P20 <input type="checkbox"/> 58</p> <p>P21 <input type="checkbox"/> 59</p> <p>P22 <input type="checkbox"/> 60</p> <p>P23 <input type="checkbox"/> 61</p> <p>P24 <input type="checkbox"/> 62</p> <p>P25 <input type="checkbox"/> 63</p> <p>P26 <input type="checkbox"/> 64</p> <p>1 <input type="checkbox"/></p> <p>2 <input type="checkbox"/></p> <p>3 <input type="checkbox"/></p> <p>4 <input type="checkbox"/></p> <p>5 <input type="checkbox"/></p> <p>6 <input type="checkbox"/></p> <p>7 <input type="checkbox"/></p> <p>SEG7</p> <p>SEG6</p> <p>SEG5</p> <p>SEG4</p> <p>SEG3</p> <p>SEG2</p> <p>SEG1</p> <p>8 <input type="checkbox"/></p> <p>9 <input type="checkbox"/></p> <p>10 <input type="checkbox"/></p> <p>11 <input type="checkbox"/></p> <p>12 <input type="checkbox"/></p> <p>13 <input type="checkbox"/></p> <p>14 <input type="checkbox"/></p> <p>SEG0</p> <p>SEG21</p> <p>SEG20</p> <p>COM3</p> <p>COM2</p> <p>COM1</p> <p>COM0</p> <p>CP1</p> <p>CP2</p> </div> </div> <div style="margin-top: 10px;"> <p>48 <input type="checkbox"/> PD4/OSCA</p> <p>47 <input type="checkbox"/> PD3/OSCA</p> <p>46 <input type="checkbox"/> VPP</p> <p>45 <input type="checkbox"/> DCLK/PD2</p> <p>44 <input type="checkbox"/> DSIO/PD1</p> <p>43 <input type="checkbox"/> DST2/PD0</p> <p>42 <input type="checkbox"/> P12/LFRO/UPMUX/EXSVD</p> <p>41 <input type="checkbox"/> P11/EXOSC/UPMUX</p> <p>40 <input type="checkbox"/> P10/FOUT/UPMUX</p> <p>39 <input type="checkbox"/> P07/REFNO/UPMUX</p> <p>38 <input type="checkbox"/> P06/REF0/UPMUX</p> <p>37 <input type="checkbox"/> P05/SENA0/UPMUX</p> <p>36 <input type="checkbox"/> P04/SENB0/UPMUX</p> <p>35 <input type="checkbox"/> Vd1</p> <p>34 <input type="checkbox"/> Cv2</p> <p>33 <input type="checkbox"/> Cv1</p> <p>32 <input type="checkbox"/> Vd2</p> <p>31 <input type="checkbox"/> VdD</p> <p>30 <input type="checkbox"/> Vss</p> <p>29 <input type="checkbox"/> #RESET</p> <p>28 <input type="checkbox"/> OSC1</p> <p>27 <input type="checkbox"/> OSC2</p> <p>26 <input type="checkbox"/> P31/TOUT11</p> <p>25 <input type="checkbox"/> P30/TOUT10</p> <p>24 <input type="checkbox"/> P03/#BZOUT/UPMUX</p> <p>23 <input type="checkbox"/> P02/BZOUT/UPMUX</p> <p>22 <input type="checkbox"/> P01/CLPLS/UPMUX</p> <p>21 <input type="checkbox"/> P00/REMO/UPMUX</p> <p>20 <input type="checkbox"/> VdD</p> <p>19 <input type="checkbox"/> Vc3</p> <p>18 <input type="checkbox"/> Vc2</p> <p>17 <input type="checkbox"/> Vc1</p> </div>			

