

S5U1C17702 Hardware Manual errata

ITEM: Usage restrictions for SPI slave-mode			
Object manuals	Document codes	Items	Pages
S5U1C17702 Hardware Manual	411545601a	Section 12 (Table 12.1)	23

(Error)

A part of "SPI" on Table 12.1 of Section 12 "Serial Ports" (pp.23)

Interface	Signal Name (Port Pin) I/O
SPI	SDI (P20)
	SDO (P21)
	SDPICK (P22)
	#SPISS (P17)

(Correct)

A part of "SPI" on Table 12.1 of Section 12 "Serial Ports" (pp.23)

Interface	Signal Name (Port Pin) I/O
SPI^{*1}	SDI (P20)
	SDO (P21)
	SDPICK (P22)
	#SPISS (P17)

***1. Note that the SPI on IC of SVT17702 can not be used. Please contact Seiko EPSON if you use or evaluate SPI on IC of SVT17702 as slave-mode.**

ITEM: Usage restrictions for SPI slave-mode			
Object manuals	Document codes	Items	Pages
S5U1C17702 Hardware Manual	411545601a	Section 14 (Figure 14.1)	25
<p>(Error)</p> <p>A part of SPI Block in a block of S1C17702 on Figure 14.1 of Section 14 "Expansion Interface" (pp.25)</p> <div data-bbox="228 672 469 723" style="border: 1px solid black; padding: 5px; width: fit-content; margin: 10px auto;">SPI</div>			
<p>(Correct)</p> <p>A part of SPI Block in a block of S1C17702 on Figure 14.1 of Section 14 "Expansion Interface" (pp.25)</p> <div data-bbox="228 1153 469 1205" style="border: 1px solid black; padding: 5px; width: fit-content; margin: 10px auto;">SPI^{*1}</div> <p>*1. Note that the SPI on IC of SVT17702 can not be used. Please contact Seiko EPSON if you use or evaluate SPI on IC of SVT17702 as slave-mode.</p>			